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TO:	FROM:	
Examiner Ted Vo	Andrew J. Dillon, Reg. No. 29,634	
ORGANIZATION:	DATE:	
US Patent and Trademark Office	November 16, 2004	
ART UNIT:	CONFIRMATION NO.:	TOTAL NO. OF PAGES INCLUDING COVER:
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FAX NUMBER:	APPLICATION SERIAL NO.:	
703-872-9306	09/550,387	
ENCLOSED:	ATTORNEY DOCKET NO.:	
Resubmission of Information Disclosure Statement	AUS000091US1	

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:  
**RENE MORALES JR.**

Serial No.: 09/550,387

Filed: 04/14/2000

For: **DATA PROCESSING SYSTEM,  
METHOD, AND PROGRAM FOR  
AUTOMATICALLY TESTING  
SOFTWARE APPLICATIONS**

§ Attorney Docket No.: AUS000091US1

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§ Examiner: **TED T. VO**

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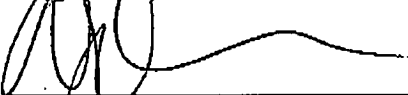
Art Unit: 2122

RESUBMISSION OF INFORMATION DISCLOSURE STATEMENT

An Information Disclosure Statement was inadvertently filed on April 14, 2000 without proper signature from the attorney of record, Andrew J. Dillon. Therefore, pursuant to Examiner's request on November 11, 2004, Applicants hereby resubmit a properly signed copy of the Information Disclosure Statement under 37 C.F.R. §1.97.

No fee is believed to be required. However, in the event that any fee is required, please charge that fee and any additional fees to IBM Corporation Deposit Account No. 09-0447.

Respectfully submitted,



Andrew J. Dillon

Reg. No. 29,634

DILLON &amp; YUDELL LLP

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ATTORNEY FOR APPLICANTS

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No. AUS000091-US1

**In Re Application of:**

RENE MORALES, JR., ET AL.

Serial No. UNKNOWN

Filed: **FILED HEREWITH**

**For: DATA PROCESSING SYSTEM,  
METHOD, AND PROGRAM FOR  
AUTOMATICALLY TESTING  
SOFTWARE APPLICATIONS**

Examiner: UNKNOWN

Group No.: UNKNOWN

## INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents  
Washington, D. C. 20231

Sir:

Applicants request that the information listed on the attached Form PTO-1449 be considered by the Office during the pendency of the above-entitled application, pursuant to 37 C.F.R. 1.97. This Information Disclosure Statement is being filed under 37 C.F.R. § 1.97(b) within three months of the filing date of the application, or before the mailing date of a first office action on the merits. No fee or certification is required.

**CERTIFICATE OF MAILING**  
**37 CFR 1.8(A)**

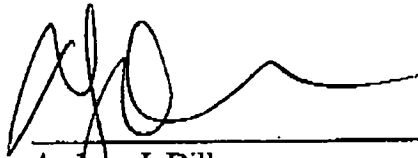
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4/14/00  
Date

Chris. Moulton

No fees are believed to be necessary. Please charge any fees that may be necessary for prosecution of the present application to IBM Corporation Deposit Account No. 09-0447.

Respectfully submitted,



Andrew J. Dillon

Reg. No. 29,634

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Sheet 1 of 2

Form PTO 1449  
Rev 7-80U.S. Dept of Commerce  
Patent & Trademark Off.Atty. Docket No.  
AUS000091US1Serial No.  
UNKNOWN**INFORMATION DISCLOSURE CITATION  
IN AN APPLICATION**Applicant  
Rene Morales, Jr., et al.Filing Date  
04/14/2000Group  
UNKNOWN**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
A	4,970,725	11/13/90	McEnroe et al.	371/15.1	03/14/89
B	5,136,705	08/04/92	Stubbs et al.	395/575	06/10/91
C	5,446,652	08/29/95	Peterson et al.	364/578	04/27/93
D	5,500,941	03/19/96	Gil	395/183.14	07/06/94
E	5,742,754	04/21/98	Tse	395/183.14	03/05/96
F	5,778,230	07/07/98	Wimble et al.	395/704	11/13/95
G	5,790,117	08/04/98	Halviatti et al.	345/333	12/08/95
H	5,913,023	06/15/99	Szerner	395/183.14	06/30/97

**FOREIGN PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
I	WO 98/57187	12/17/98	Proskauer et al.		06/13/97

**PUBLICATIONS OR OTHER INFORMATION**

EXAMINER INITIAL	(INCLUDE AUTHOR, TITLE, DATE, PERTINENT PAGE, ETC)
J	"System Verification Using Multiple Independent Simulation Engines Running Under Common Control," IBM Technical Disclosure Bulletin, Vol. 30 No. 01 June 1987, pgs. 340-342.
K	"Software Validation Test Facilitator "test scripts," IBM Technical Disclosure Bulletin, Vol. 35 No. 4B September 1992, pgs. 212-215.
L	"Automated Code Checker," IBM Technical Disclosure Bulletin, Vol. 36 No. 09B September 1993, pgs. 239-240.
M	"Functional Test Case Identification in an Object-Oriented Environment using Matrix Progression Techniques," IBM Technical Disclosure Bulletin, Vol. 37 No. 01 January 1994, pgs. 503-505.
N	"Error Detection/Fault Isolation in VHDL," IBM Technical Disclosure Bulletin, Vol. 39 No. 03 March 1996, pg 27.

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UNKNOWN

## U.S. PATENT DOCUMENTS

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## FOREIGN PATENT DOCUMENTS

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## PUBLICATIONS OR OTHER INFORMATION

EXAMINER INITIAL	(INCLUDE AUTHOR, TITLE, DATE, PERTINENT PAGE, ETC)
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IO "Advanced Computer System Evaluation Architecture." IBM Technical Disclosure Bulletin,  
Vol. 37 No. 10 October 1994, pgs. 467-468

IP "Method of Controlling Development Phases of Computer Software." IBM Technical  
Disclosure Bulletin, Vol. 33 No. 1B June 1990, pgs. 114-116.

IQ "Functional Test Case Identification in an Object-Oriented Environment using Matrix  
Progression Techniques." IBM Technical Disclosure Bulletin, Vol. 37 No. 01 January 1994,  
pgs. 503-505

IR "Defect Isolation." IBM Technical Disclosure Bulletin, Vol. 37 No. 04A April 1994,  
pgs. 239-240.

IS "Non-Invasive Automation of Software Test With Shared Peripherals." IBM Technical  
Disclosure Bulletin, Vol. 35 No. 2 July 1992, pg 230-232.

IT "System and Method For Portable Server-Based Active Process Flow Software  
Components." Research Disclosure, April 1999, pg. 567.

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DATE CONSIDERED

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